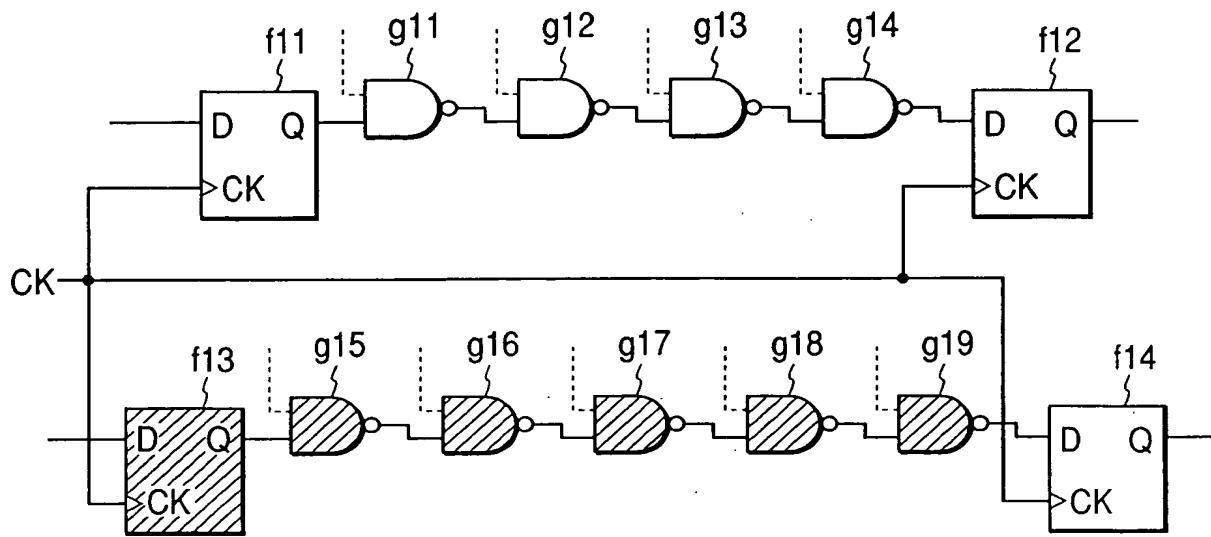
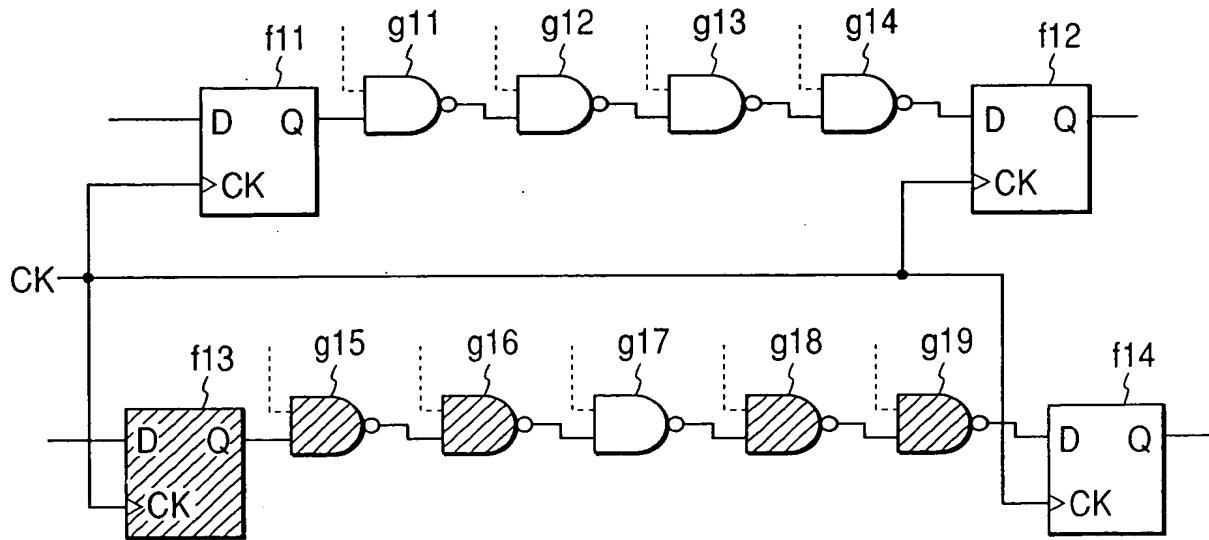


***FIG. 1******FIG. 2***

2018-2019 SEHITTA GİDÖKT

FIG. 3

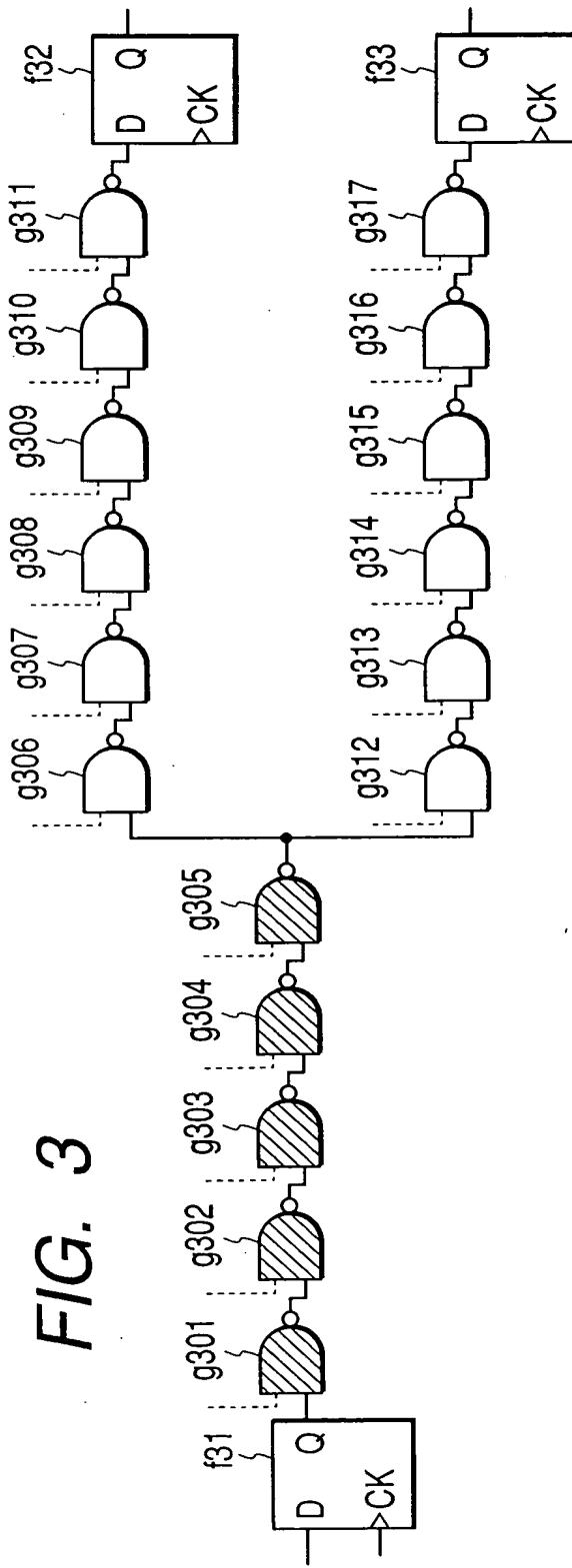
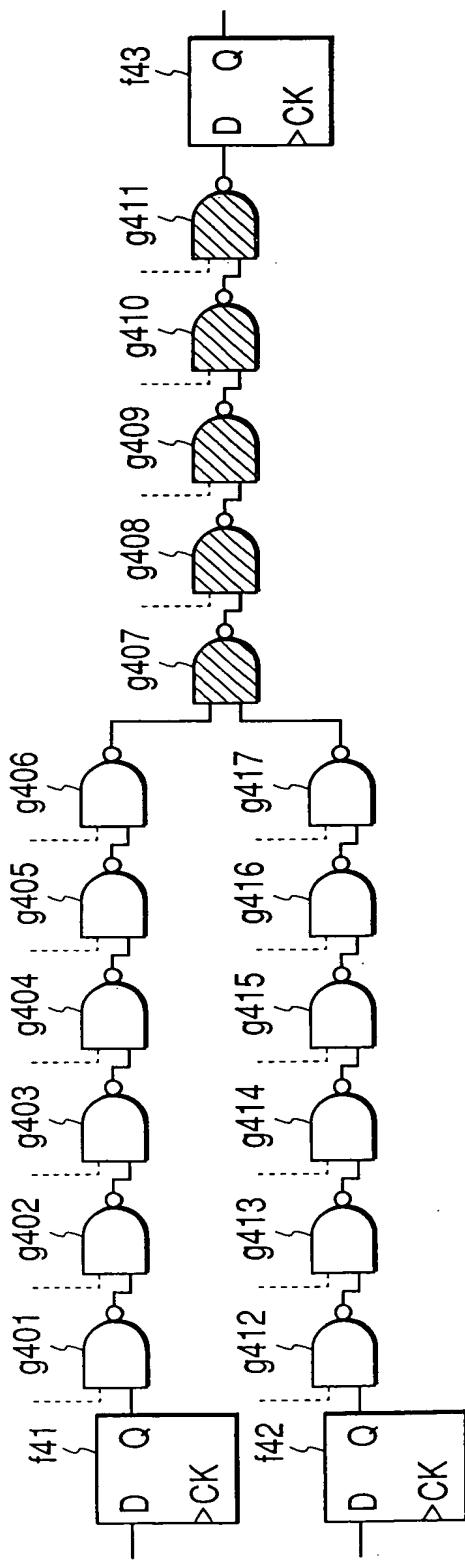
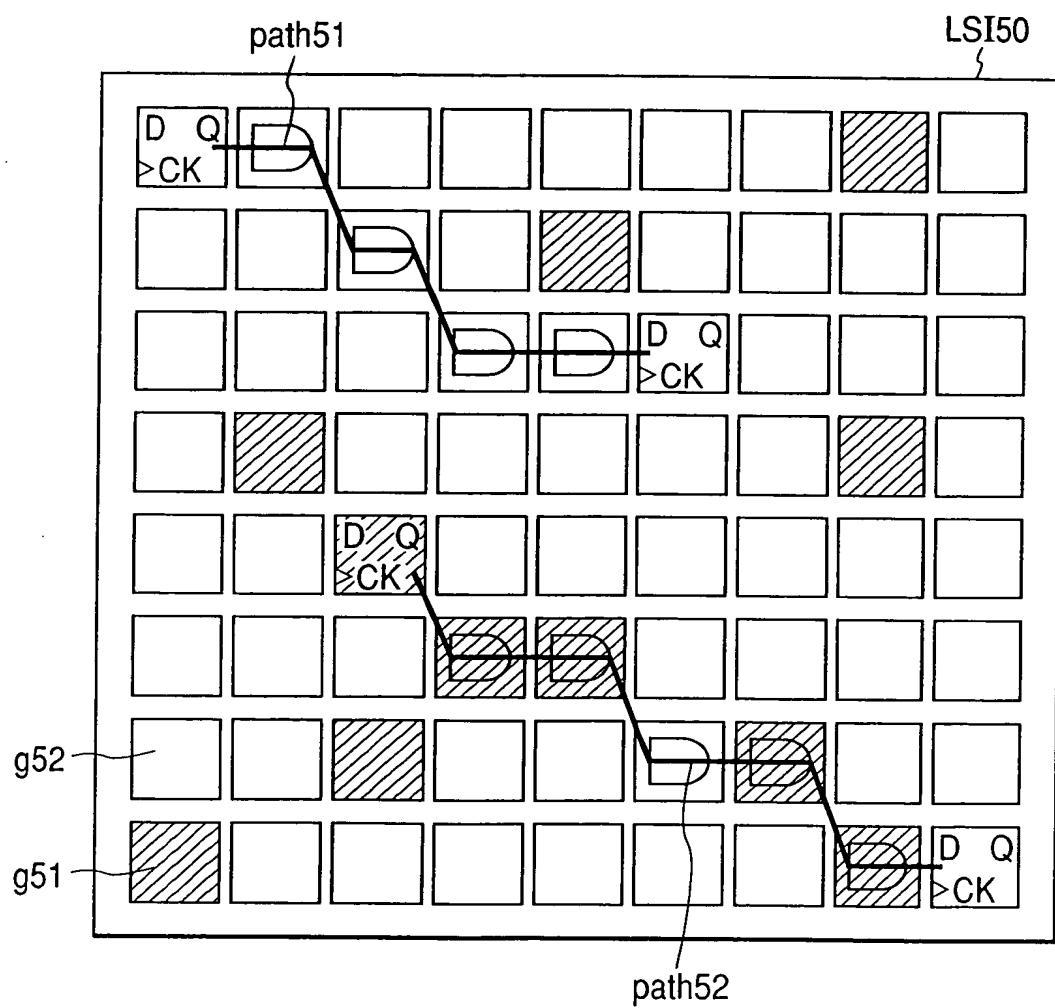


FIG. 4



**FIG. 5**

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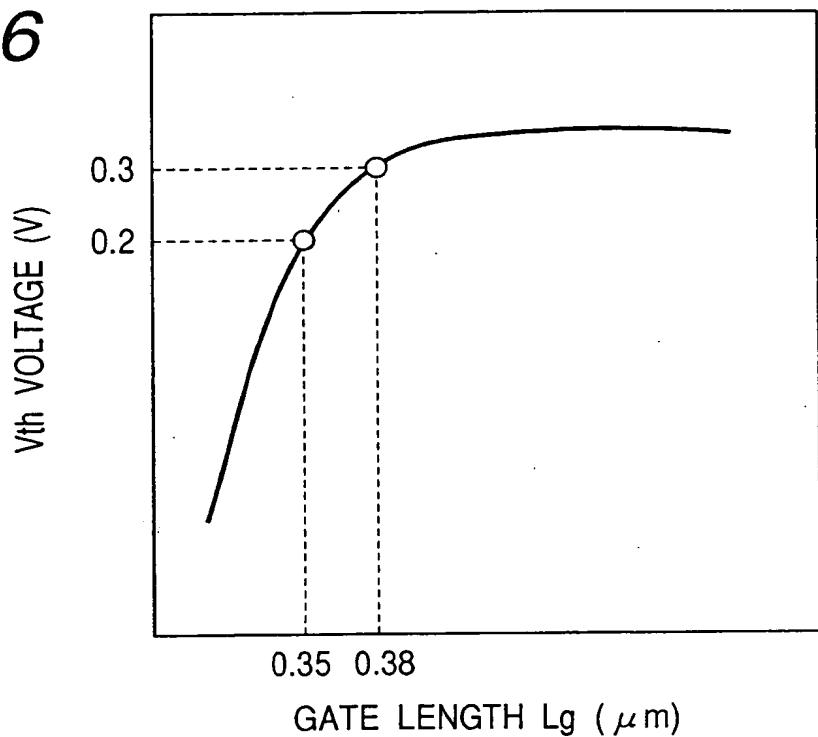
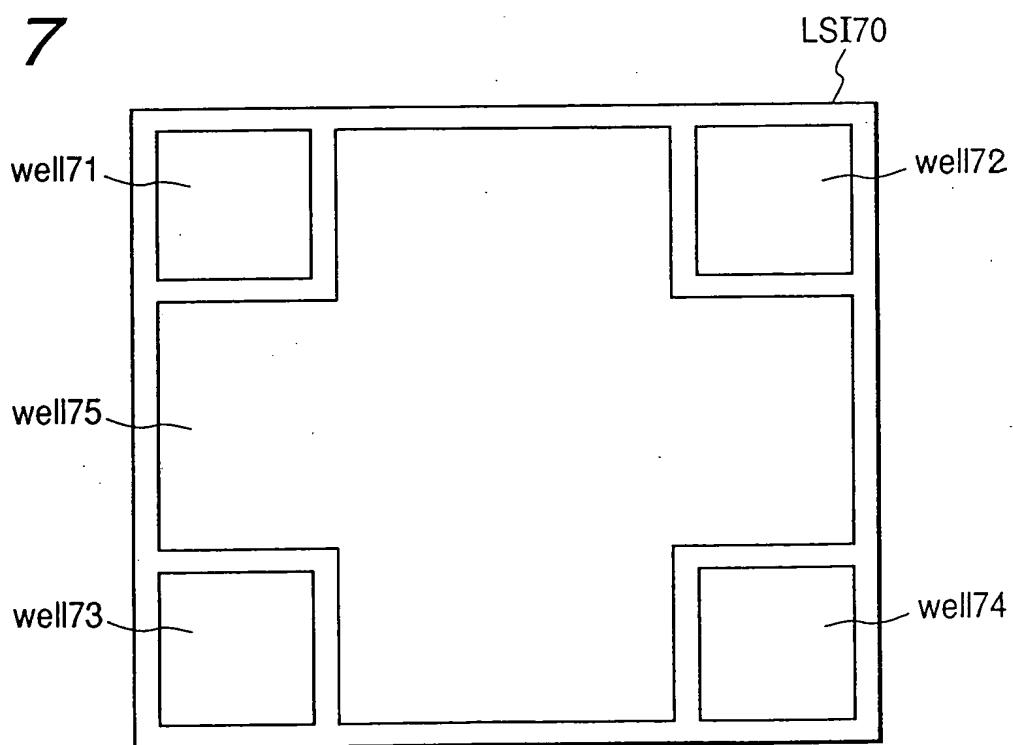
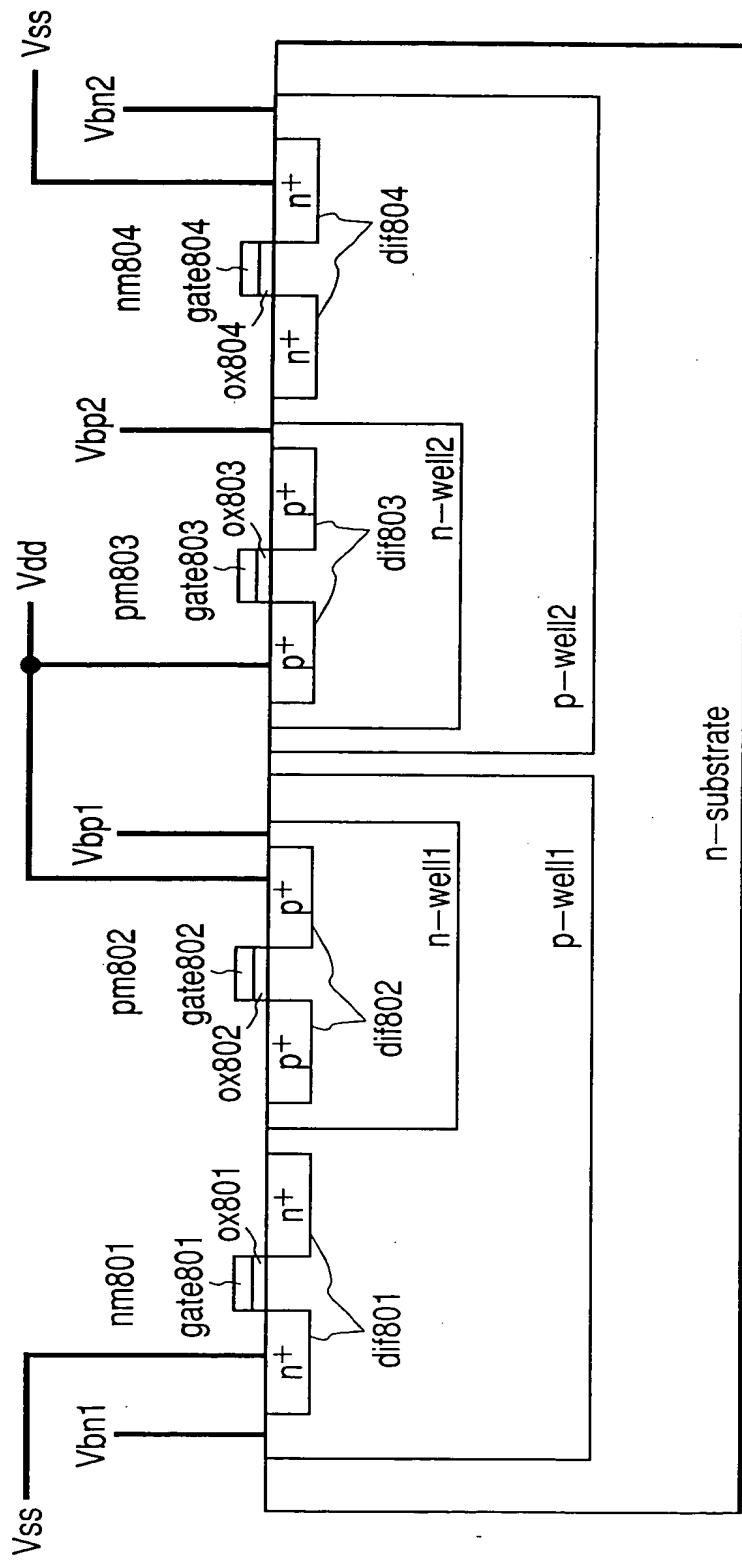
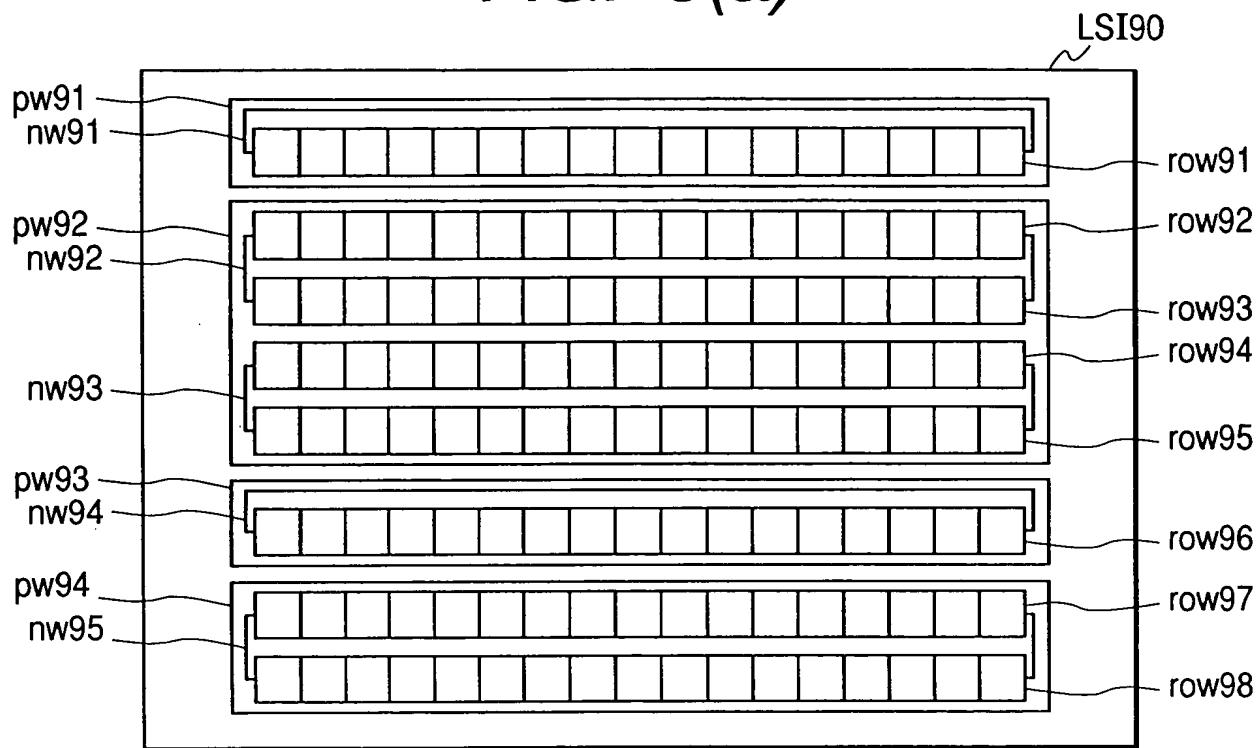
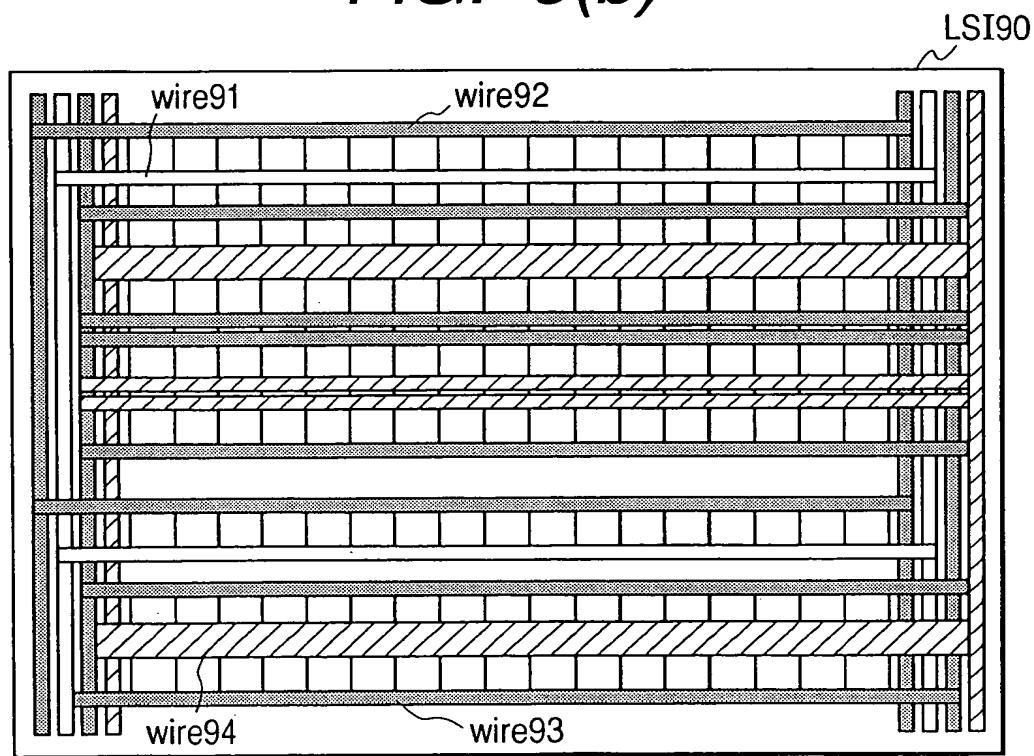
**FIG. 6****FIG. 7**

FIG. 8



*FIG. 9(a)**FIG. 9(b)*

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**FIG. 10**

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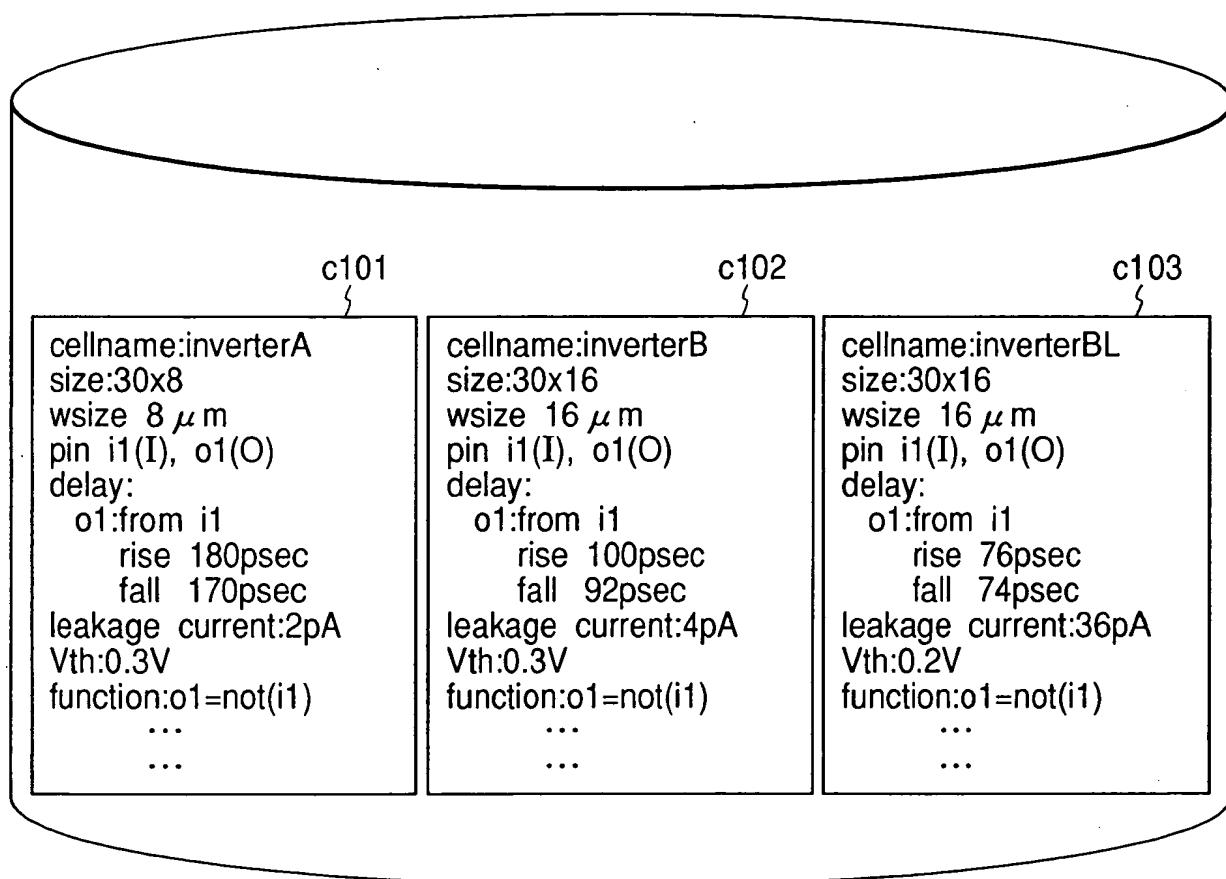
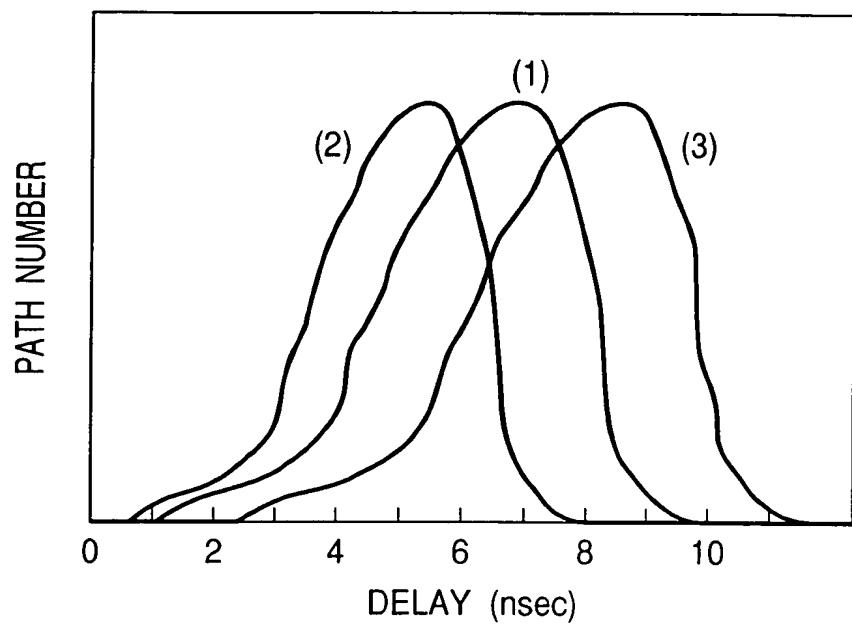


FIG. 11



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FIG. 12

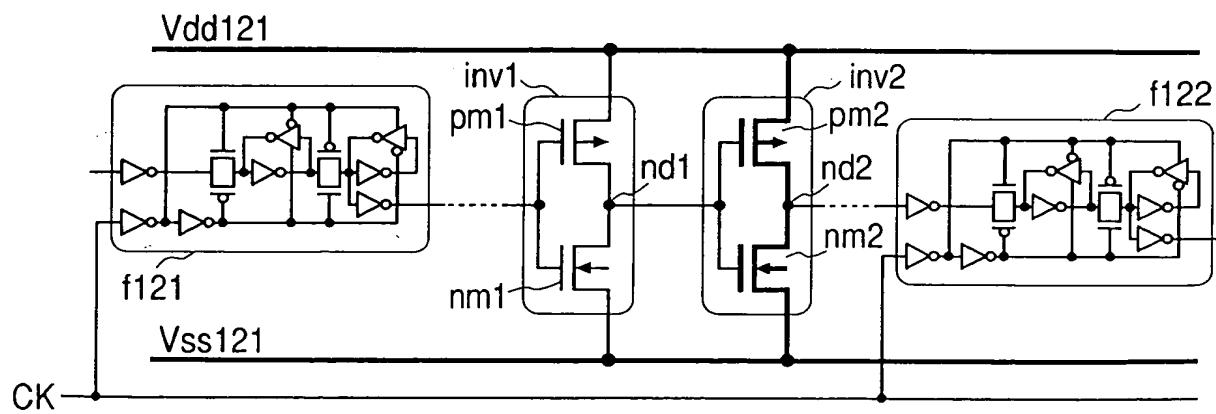


FIG. 13

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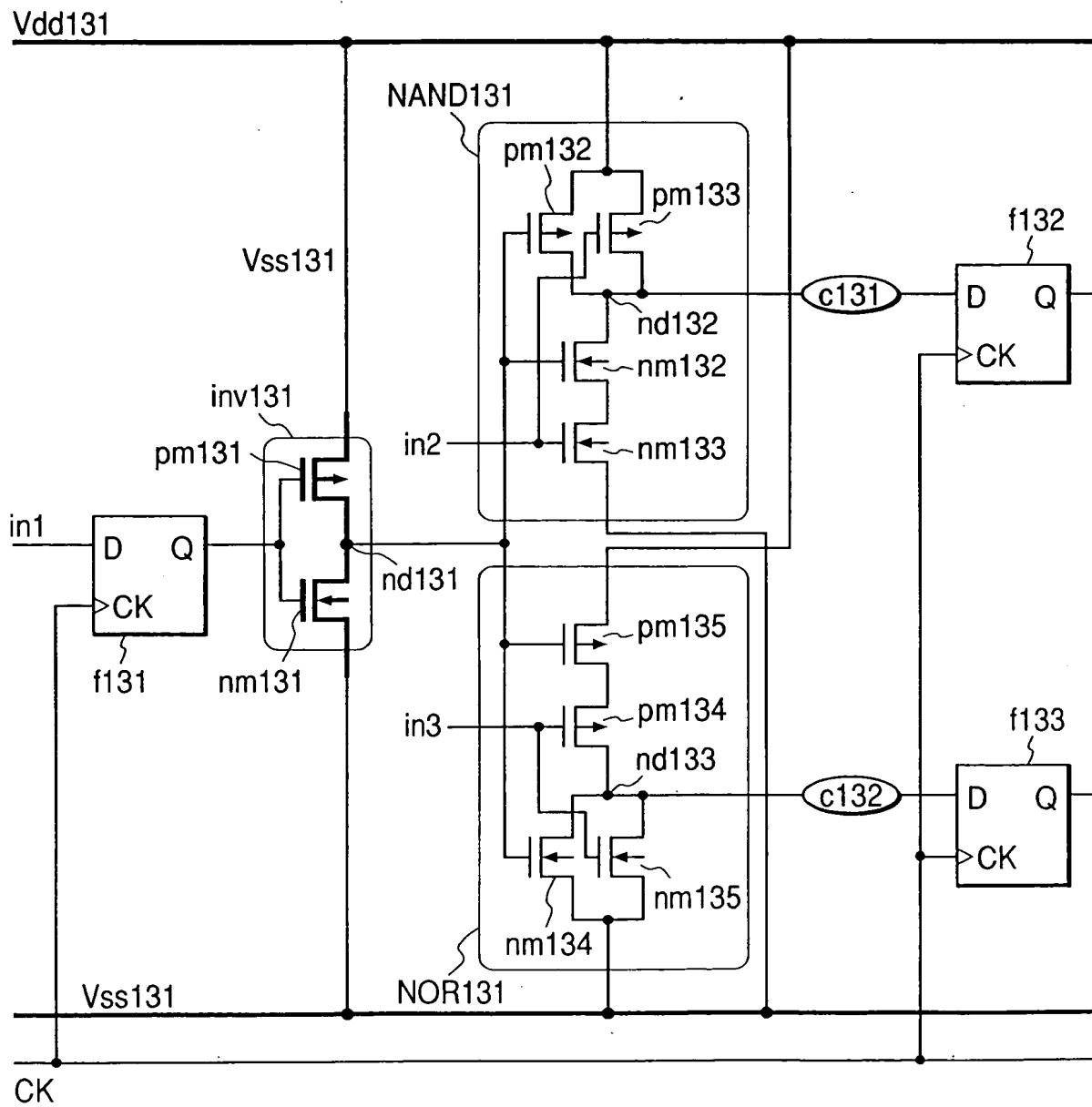
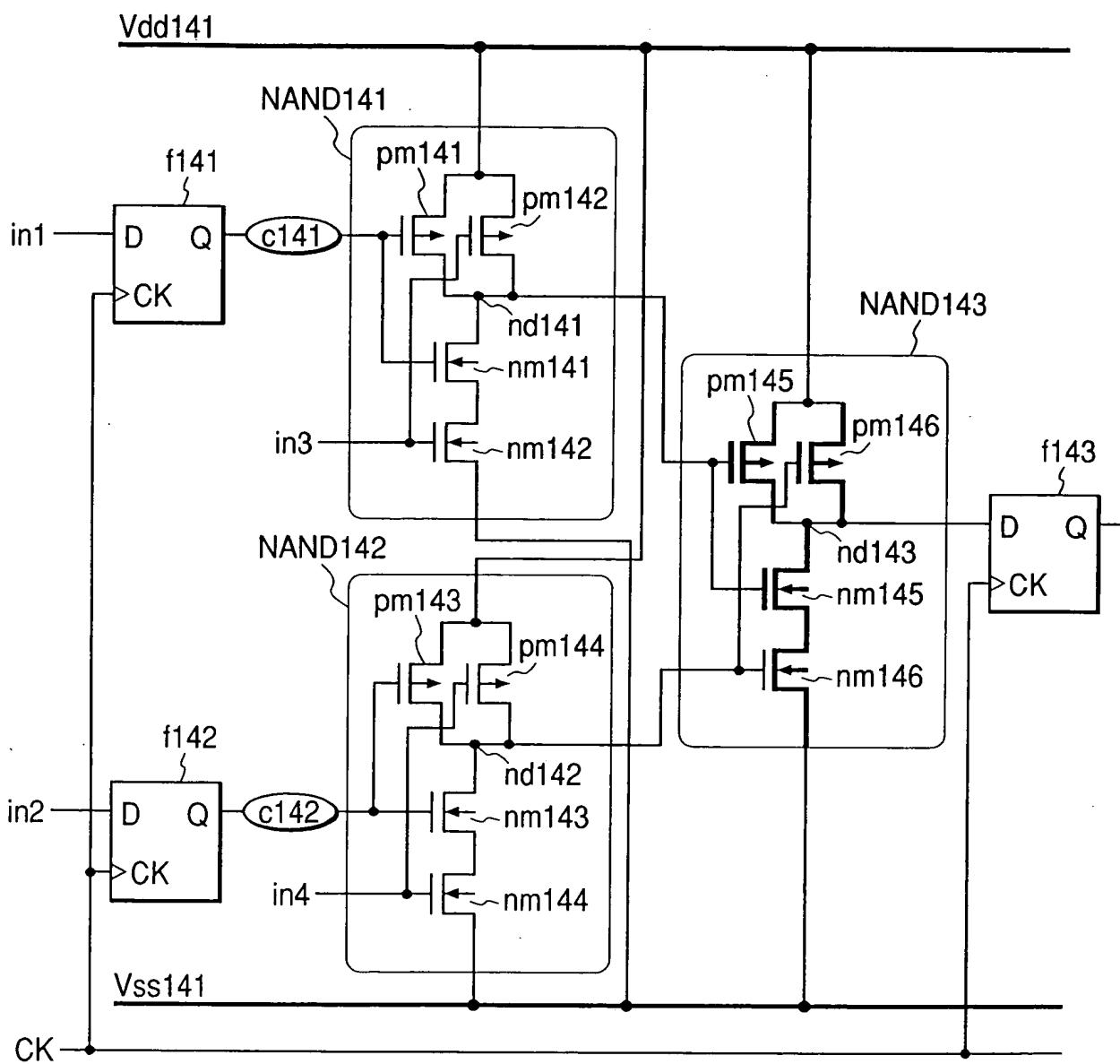
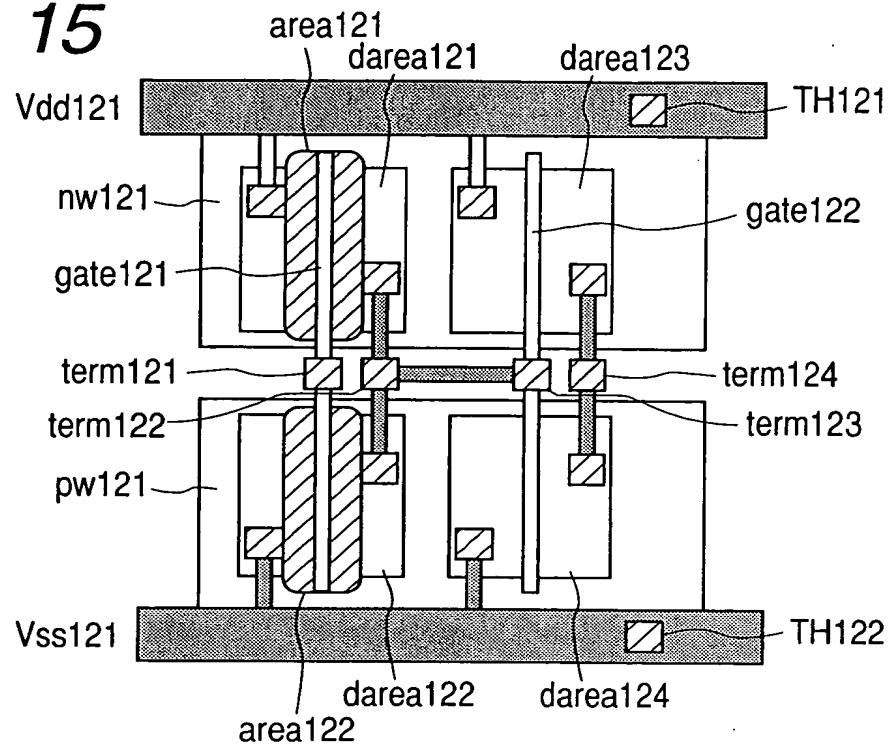
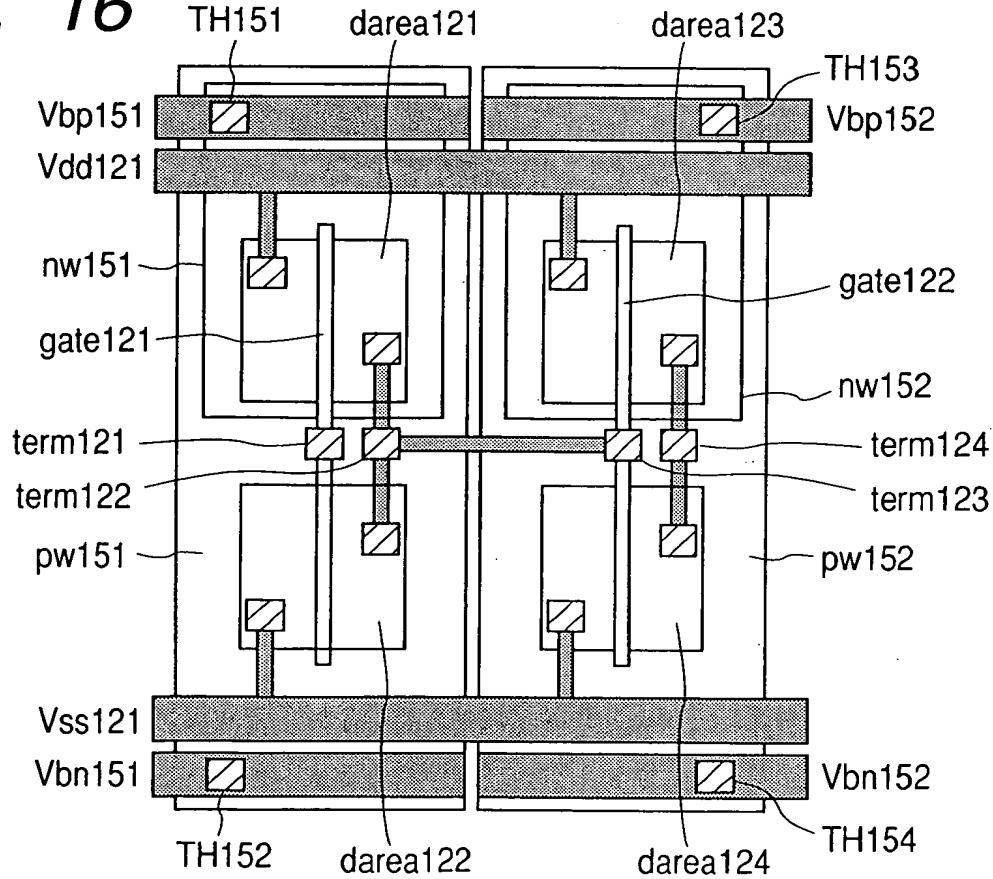
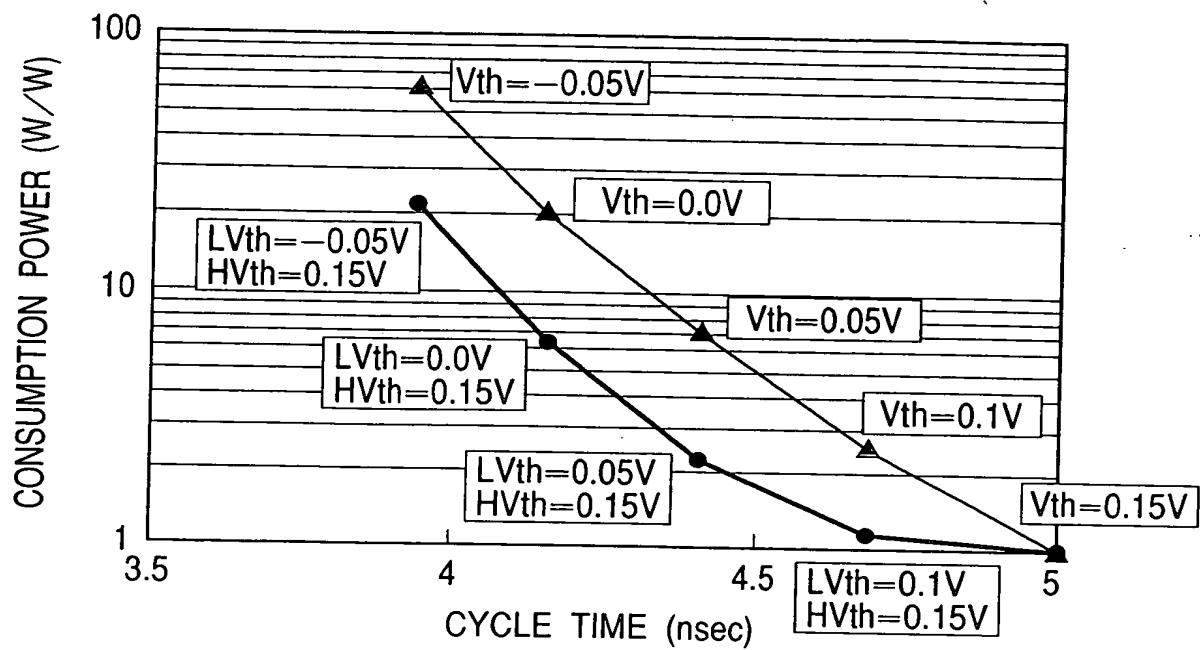
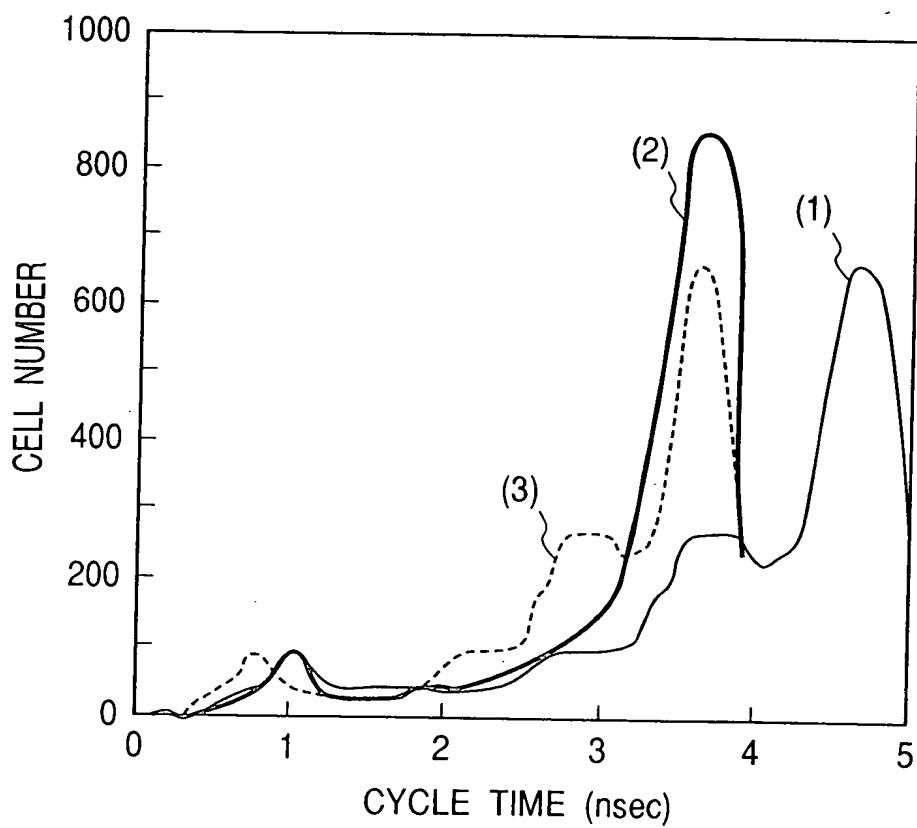


FIG. 14

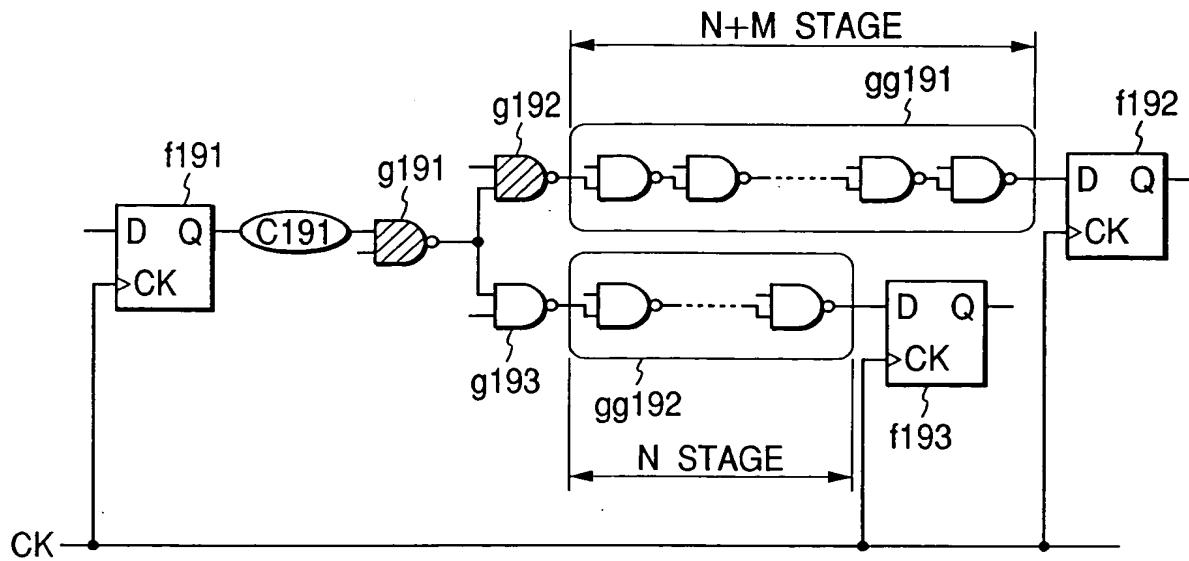
TOP SECRET//SI



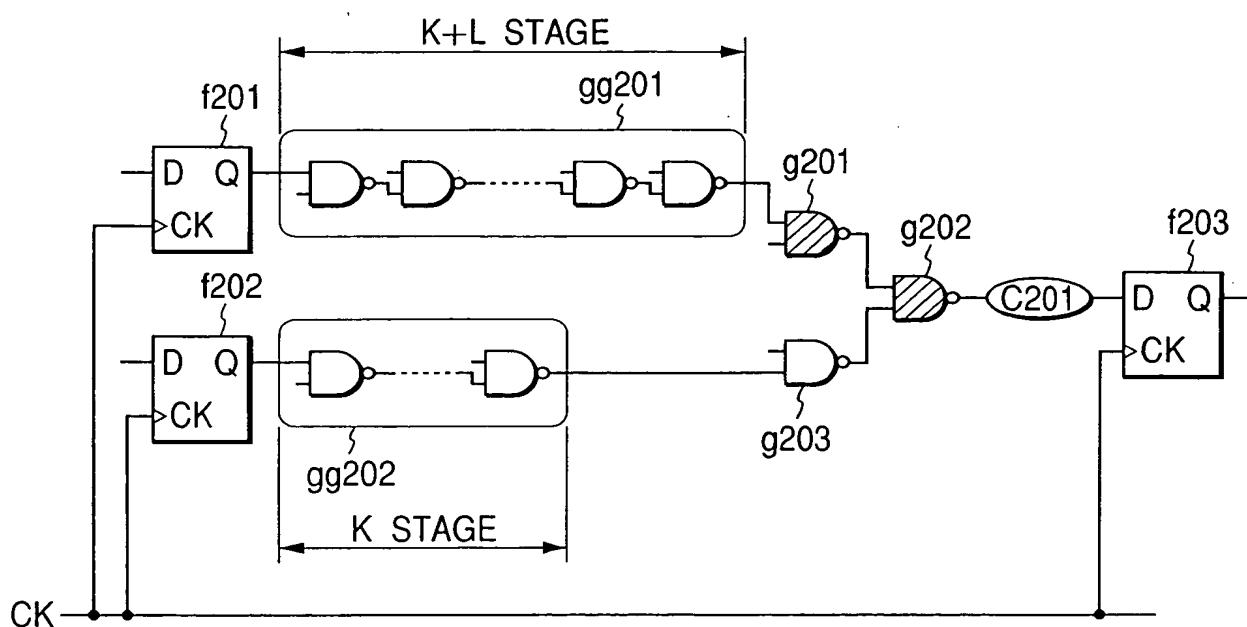
**FIG. 15****FIG. 16**

**FIG. 17****FIG. 18**

*FIG. 19*



*FIG. 20*



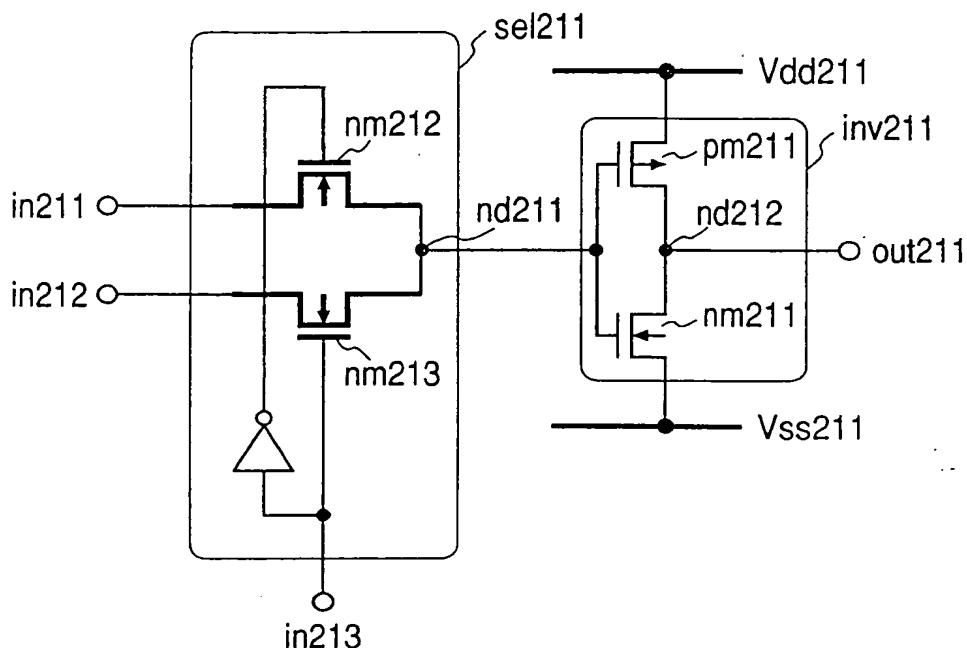
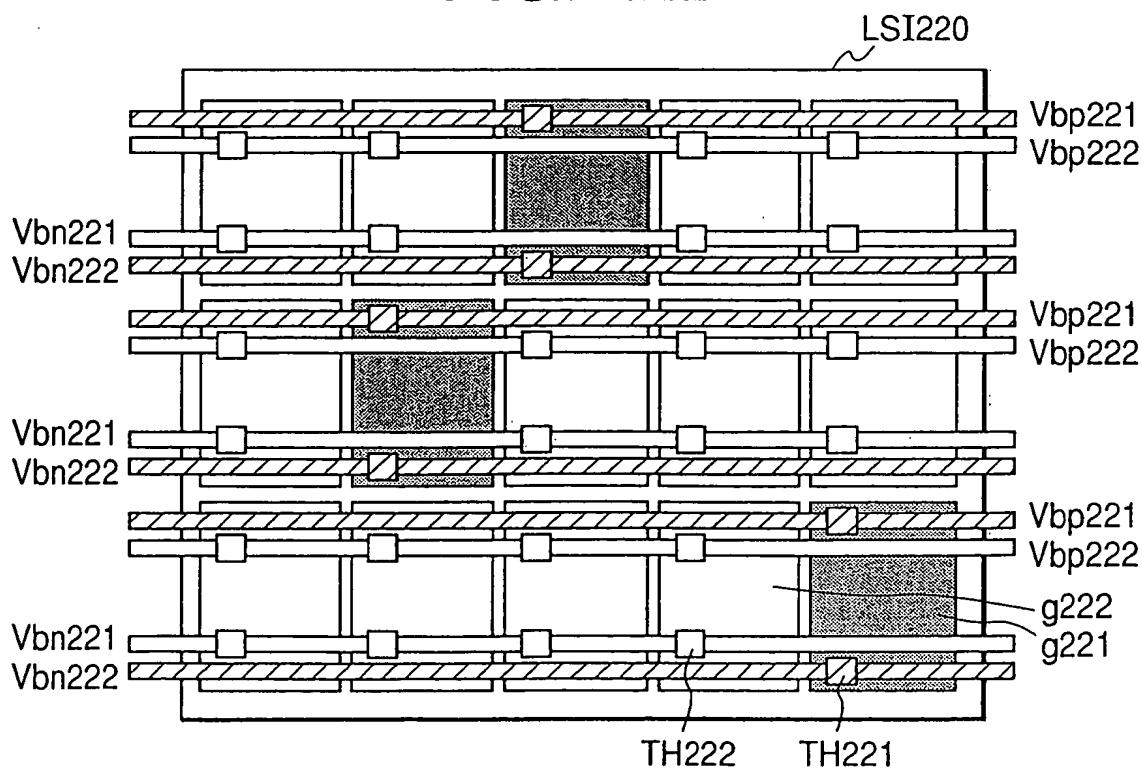
**FIG. 21****FIG. 22**

FIG. 23

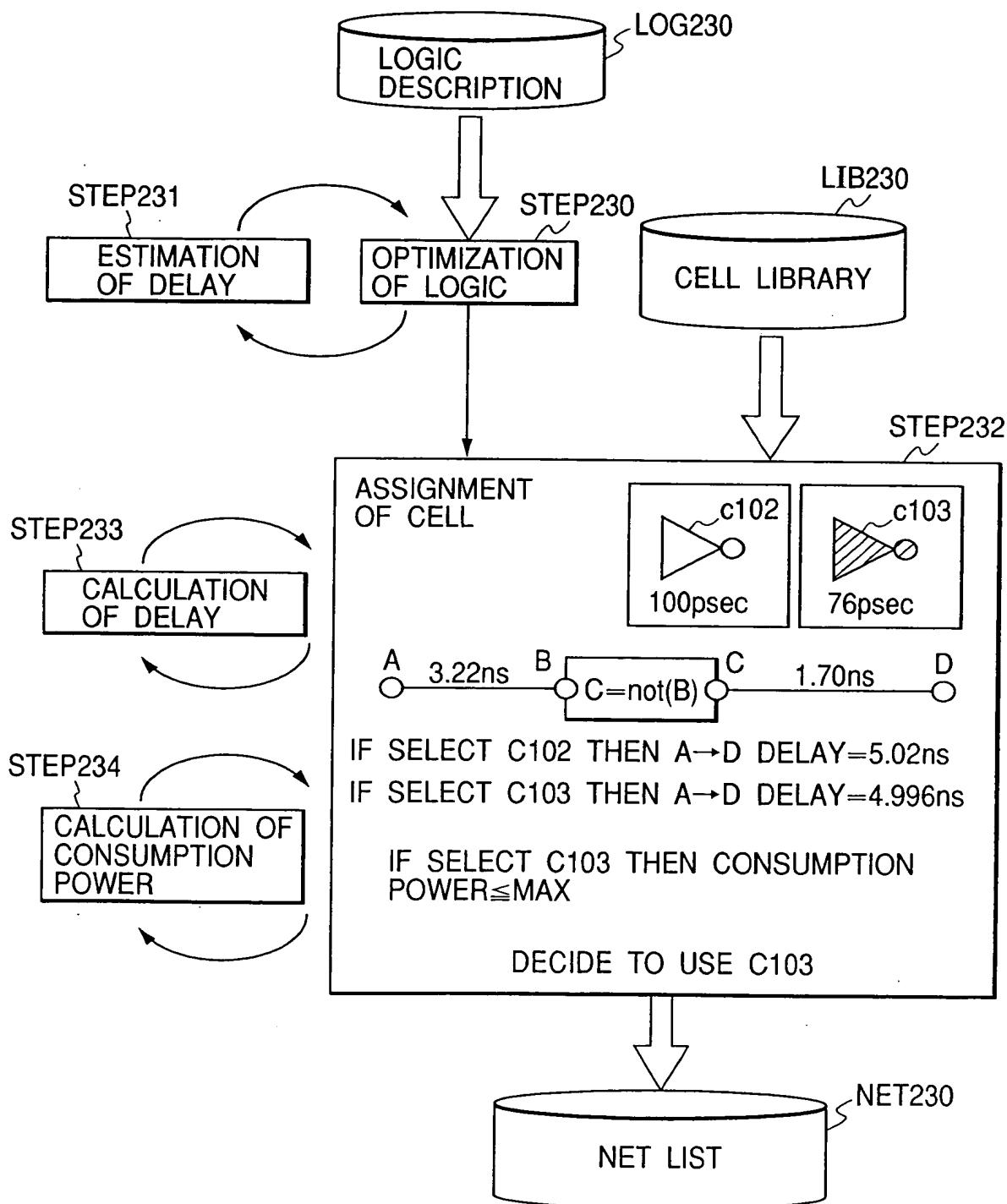
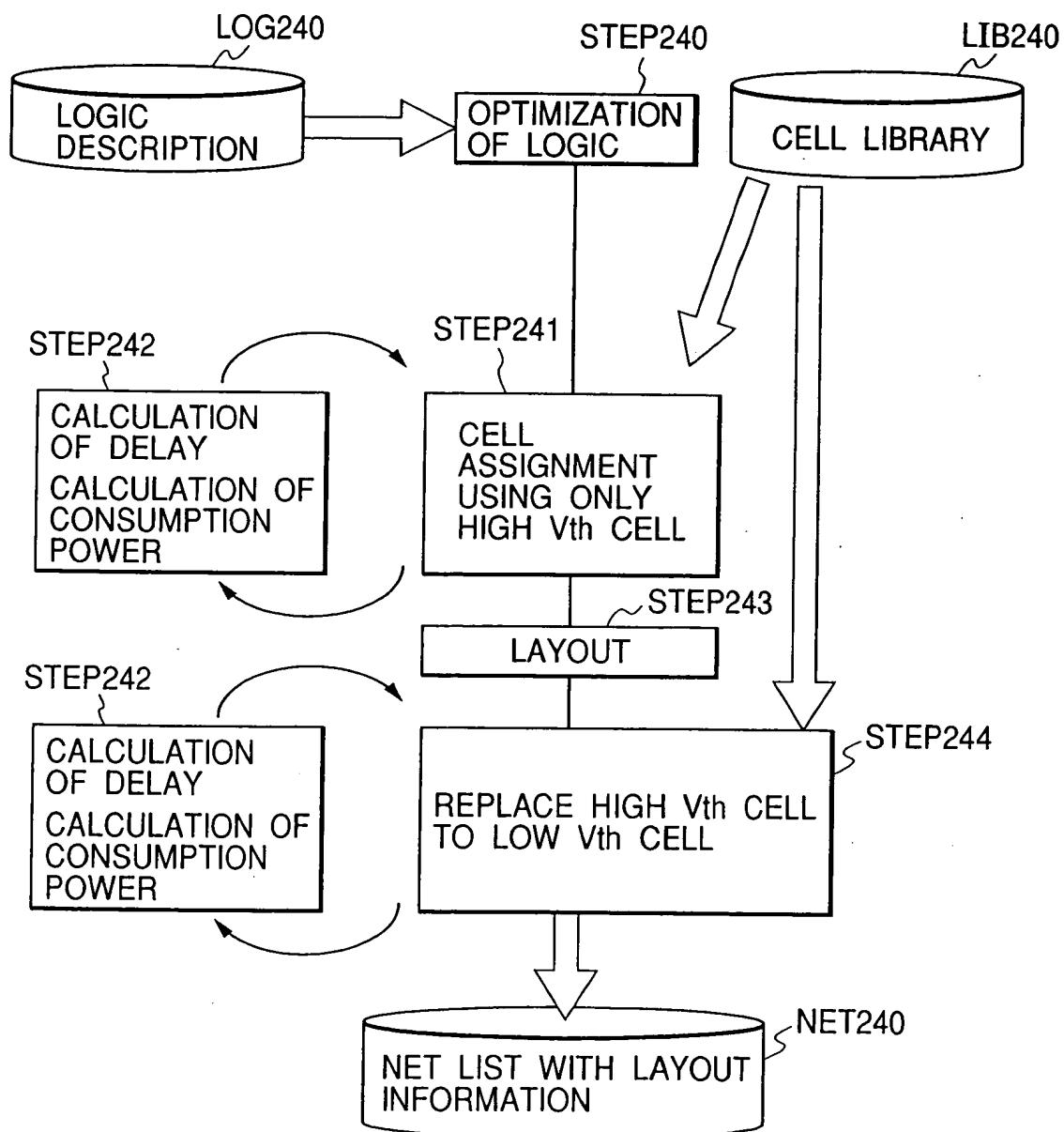
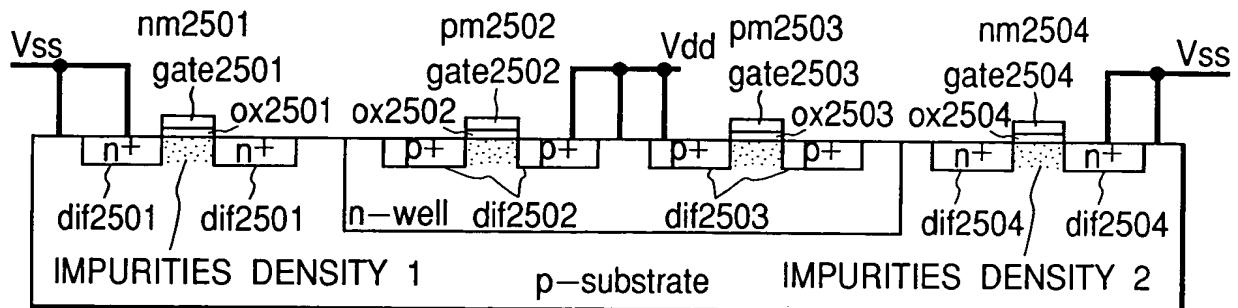
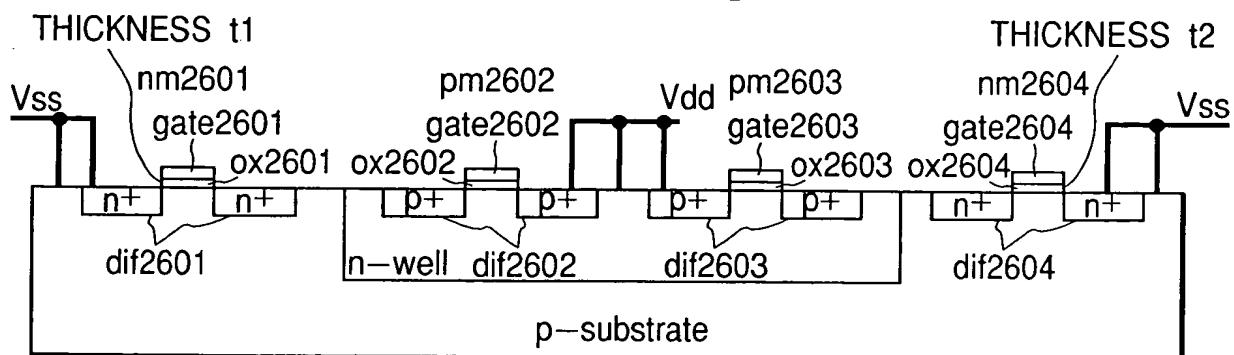
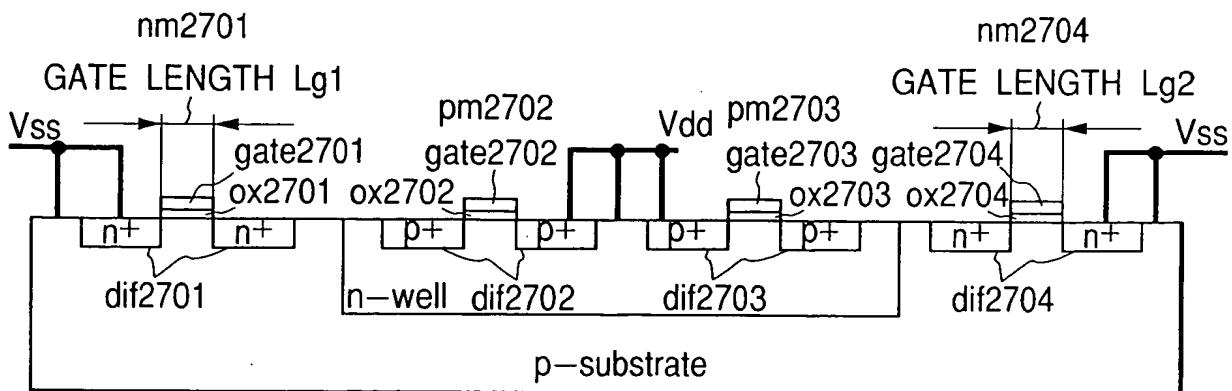
1. LOGIC DESIGN  
2. OPTIMIZATION  
3. CELL LIBRARY  
4. NET LIST

FIG. 24

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**FIG. 25****FIG. 26****FIG. 27**

*FIG. 28*

